

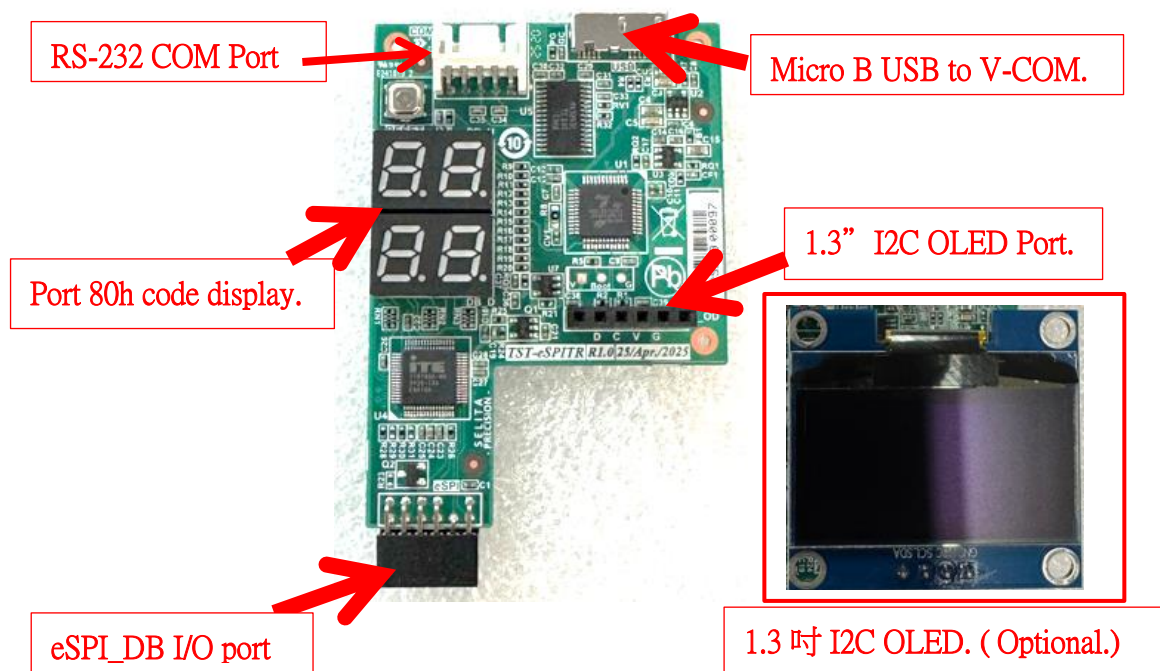
TST-eSPITR eSPI POST Code 測試卡



一、TST-eSPITR 產品特色：

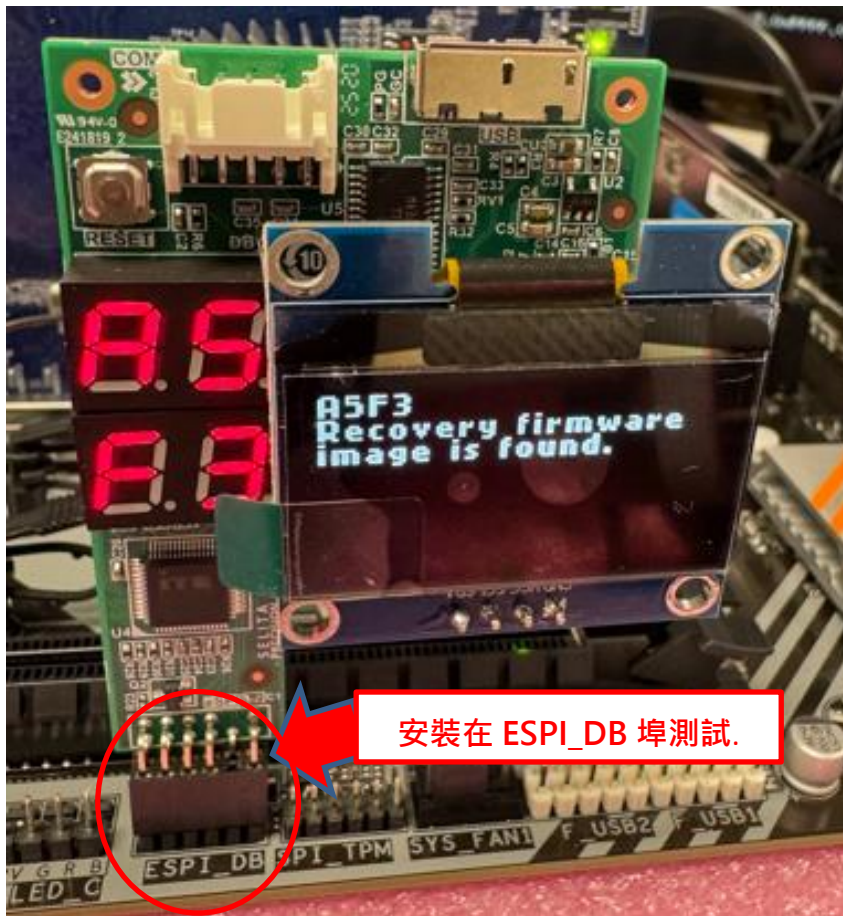
- 採用 IT8786E-80 eSPI Interface PORT80 debug card chip.
- Compatible with eSPI specification v1.0.
- 採用 32-bit Arm® Cortex®-M0+ processor core 輔助解析 80h 資訊.
- 支援 1.3 吋 I2C OLED 顯示.
- 自動記錄 20 組 post code.
- 100%相容技嘉 AMD 主機板 eSPI_DB port.

二、TST-eSPITR 產品介紹：

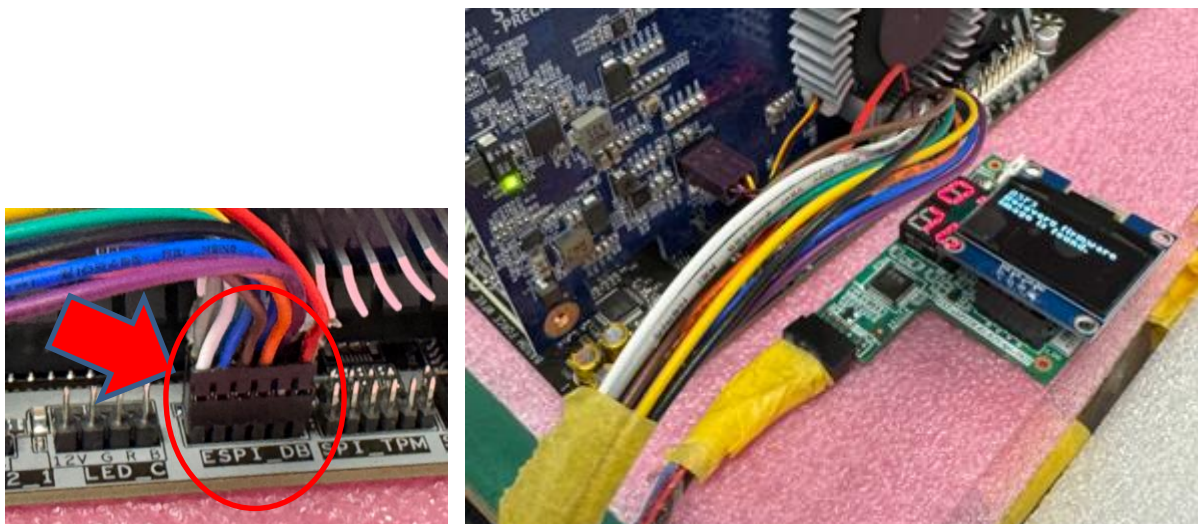


三、TST-eSPITR 使用說明：

1. 將治具安裝於主機板 ESPI_DB Port，開機即自動解析跑碼。

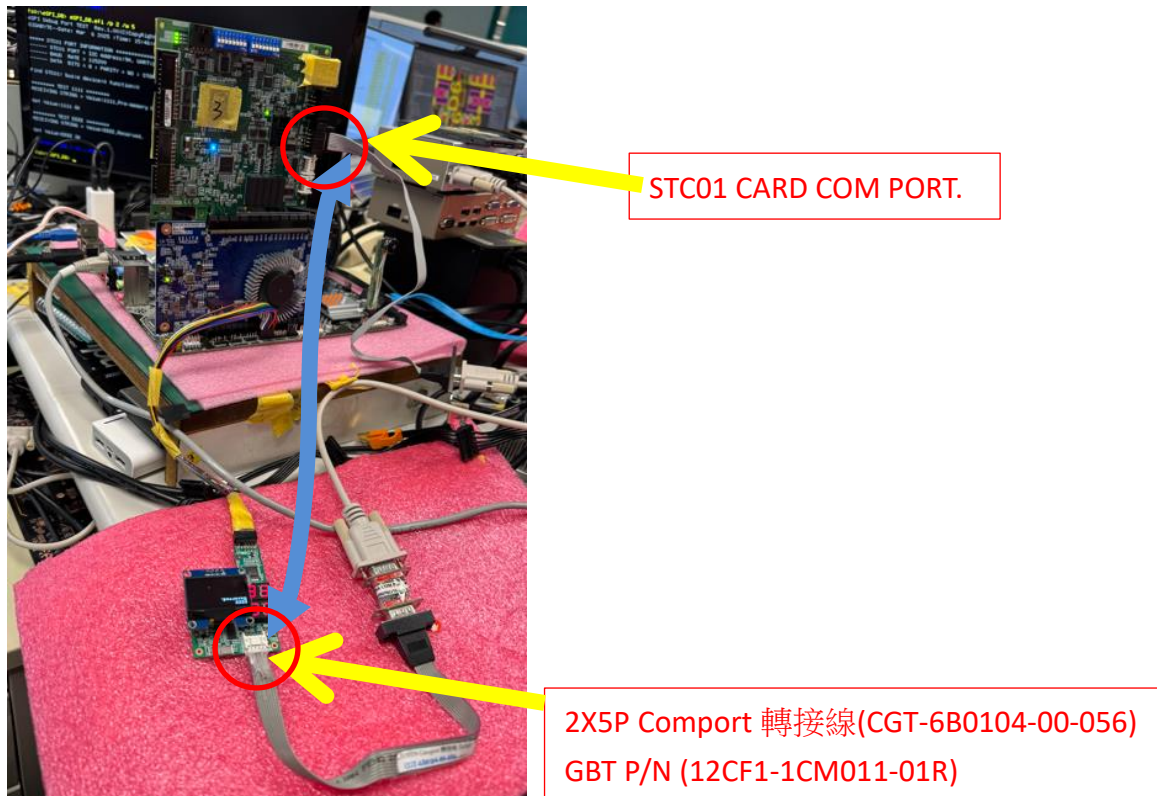


2. 若主板周邊有其它裝置卡干涉，可串接 ESPI_DB 轉接線 (CGD-5B0929-00-282)



四、TST-eSPITR UEFI 測試說明：

1. 治具安裝 2x5 COM CABLE 轉接線，連接至 STC01 卡 COM port.



2. UEFI 下使用 eSPI_DB.efi 程式測試

- 2-1. 執行 eSPI_DB.efi 即顯示程式相關參數說明，如下圖畫面:

```
fs0:\eSPI_DB> eSPI_DB.efi
eSPI Debug Port TEST Rev.1.00(C)CopyRight by Giga-Byte Technology Co.
GIGABYTE--Date: Mar 6 2025 :Time: 15:46:44 * Edited by Ksd.h--GIGABYTE
HELP message:
Usage: eSPI_DB.efi [/P Port] [/B Baud] [?]
PORT:
    1./P Port :The port is set port number 1~8.
    2./B Baud :The Baud of baud bps,default is 115200.
    3./W Time :The Time is test setp wait time.
    4. ?      :Display the HELP message such as this.
Example:
eSPI_DB /p 1
eSPI_DB /p 8
eSPI_DB /p 1 /W 5
eSPI_DB /?
The return value:
0:Check All PASS!
255:Check Fail!
```

2-1. 執行參數測試，範例：eSPI_db.efi /p 2 /w 3

測試程式會傳送 1111 及 EEEE 訊息並自動判斷顯示結果。

```
fs0:\eSPI_DB> espi.nsh
espi.nsh> espi_db.efi /p 2 /w 3
eSPI Debug Port TEST Rev.1.00(C)Copyright by Giga-Byte Technology Co.
GIGABYTE--Date: Mar 6 2025 :Time: 15:46:44 * Edited by Ksd.h--GIGABYTE

***** STC01 PORT INFORMATION *****
----- STC01 PORT = I2C Address:9A, UART:2
----- BAUD RATE = 115200
----- DATA BITS = 8 : PARITY = NO : STOP BIT =1

Find STC01! bus:e device:0 function:0

***** TEST 1111 *****
RECEIVING STRING = Value:1111,Pre-memory CPU initialization is started.

Get Value:1111 OK

***** TEST EEEE *****
RECEIVING STRING = Value:EEEE,Reserved.

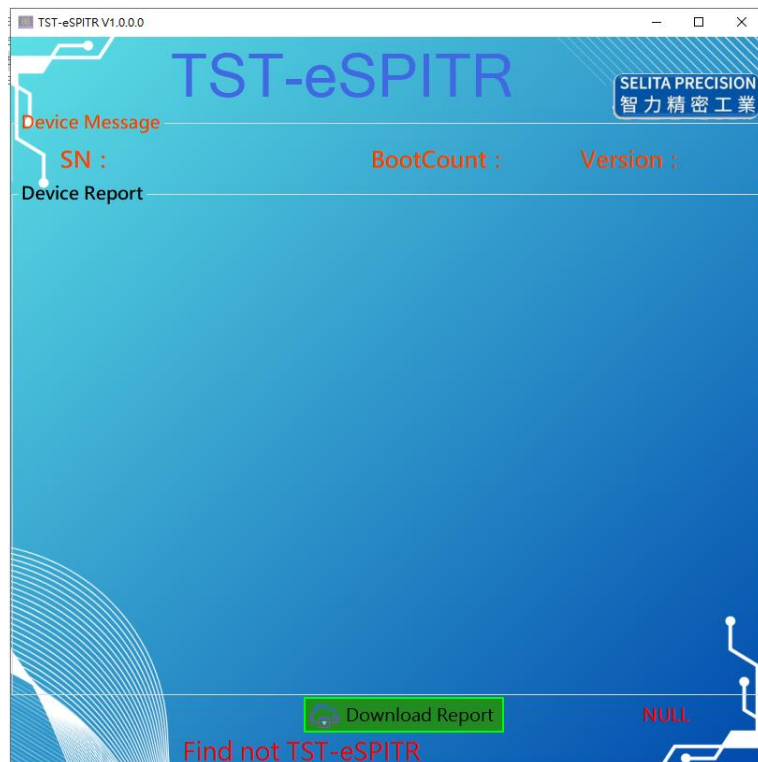
Get Value:EEEE OK

(Return Code = 0)...PASS!!
```



五、TST-eSPITR POST CODE 記錄下載說明：

1. 連接 Micro-USB 後開啟 TST-eSPITR 工具軟體..



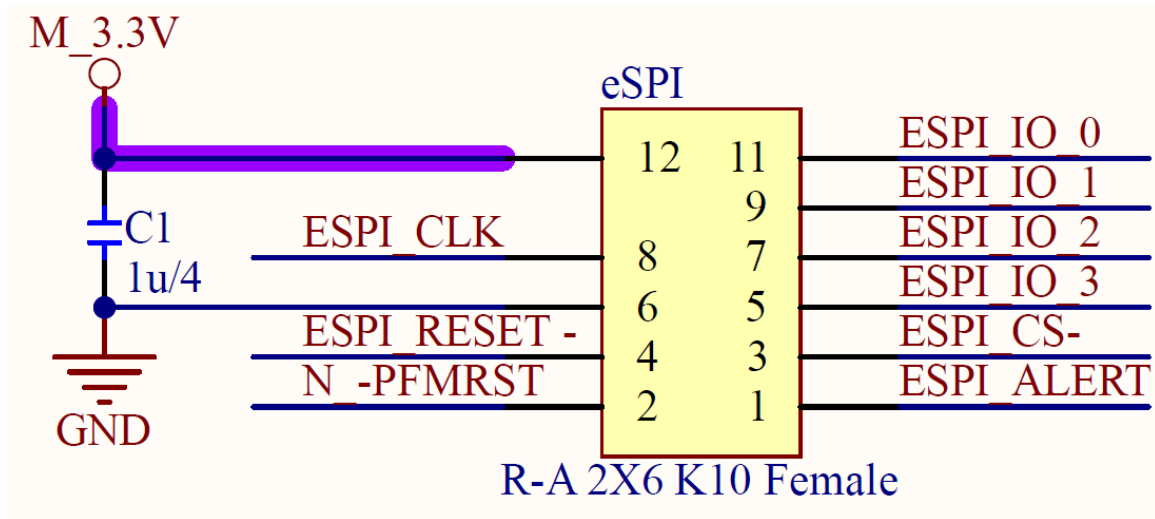
2. 連線待讀取資料後，可選擇 Download Report 另儲存報告.(excel 檔.)



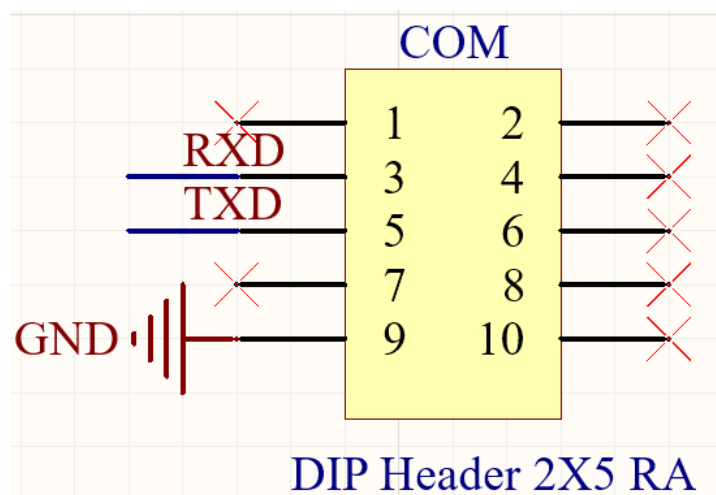
Time	code	Description
0	80	Reserved for OEM use (OEM DXE initialization codes).
0	280	Reserved for OEM use (OEM DXE initialization codes).
0	200	Unknown.
0	F000	Unknown.
0	0	Unknown.
0	F200	Unknown.
0	0	Unknown.
0	F200	Unknown.
0	100	Unknown.
0	F200	Unknown.
0	200	Unknown.
0	F100	Unknown.
0	E60C	Unknown.
0	ABE5	Unknown.
0	ABB5	PCI device hot plug.
0	E0B1	Runtime AP installation ends.
0	E0B9	Reserved.
0	E090	Phase transfer to BDS (Boot Device Selection) from DXE.
0	E60C	Unknown.
0	E001	Unknown.
0	E046	Reserved.
1	E60C	Unknown.
1	E000	Unknown.
1	0	Unknown.
1	F200	Unknown.
1	F000	Unknown.
1	F100	Unknown.
1	200	Unknown.

六、TST-eSPITR 各連接器接腳說明.

1. eSPI 2x6P P2.0 接頭腳位說明：



2. COM 2x5P P2.0 COM 接頭腳位說明：



3. Micro B USB only support USB2.0 , USB3.0 無用途：

